

Appl. No. 10/618,012
Amdt. Dated January 16, 2006
Reply to Office Action of October 18, 2005

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. – 3. (Cancelled)

4. (Currently Amended) A liquid crystal display device ~~according to claim 3,~~
comprising:

a pixel section having pixels arranged in a matrix which include active elements,
and signal lines connected to columns of pixels, and wherein each pixel has a common
electrode and a pixel electrode;

first control means for switching on the active elements for all the pixels in said
pixel section when said liquid crystal display device is in a power-off state; and

second control means for setting, in the power-off state, all the signal lines to each
have a potential equal or substantially equal to the potential of the common electrodes of
the pixels; and

wherein said first control means is a vertical scanning system which sequentially
switches on the active elements in units of rows when said liquid crystal display device is
in a normal display mode, and which simultaneously switches on the active elements in
the power-off state; and

wherein said second control means is a precharging scanning system which, in the
normal display mode, supplies a precharging signal to the pixels in the row selected by
said vertical scanning system before ~~said~~ a horizontal scanning system supplies the
display signal to the pixels in the row selected by said vertical scanning system.

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5. (Currently Amended) A liquid crystal display device comprising:

a pixel section having pixels arranged in a matrix which include active elements, and signal lines connected to columns of pixels and wherein each pixel has a common electrode and a pixel electrode; and

selecting means for selecting one of a first power-off mode and a second power-off mode in accordance with the type of power-off state of said liquid crystal display device,

wherein:

in the first power-off mode, in the power-off state, white level signals or black level signals are written in all the pixels while the pixels in said pixel section are first selected in a sequential manner in units of rows; and

in the second power-off mode, in the power-off state, the active elements for all the pixels in said pixel section are switched on and all the signal lines are set to each have a potential equal to the potential of common electrodes of the pixels.

6. (Original) A liquid crystal display device according to claim 5, further comprising:

a power-off button; and

a power-supply battery,

wherein said selecting means selects the first power-off mode when the power-off state is caused by operating said power-off button, and selects the second power-off mode when the power-off state is caused by removing said power-supply battery.

7. (Cancelled)

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8. (Currently Amended) A method for controlling a liquid crystal display device having pixels arranged in a matrix which include active elements, signal lines connected to columns of pixels, a power-off button, and a power-supply battery, and wherein each pixel has a common electrode and a pixel electrode, said method comprising the steps of:

for a power-off state caused by operating the power-off button, writing white level signals or black level signal to all the pixels while first selecting the pixels in a sequential manner; and

for a power-off state caused by removing the power-supply battery, switching on the active elements for all the pixels, and setting all the signal lines to each have a potential equal to the potential of common electrodes of the pixels.

9. (Cancelled)

10. (Currently Amended) A portable terminal comprising a liquid crystal display device used as a screen display unit, said liquid crystal display device comprising:

a pixel section having pixels arranged in a matrix which include active elements, and signal lines connected to columns of pixels, and wherein each pixel has a common electrode and a pixel electrode; and

selecting means for selecting one of a first power-off mode and a second power-off mode in accordance with the type of power-off state,

wherein:

in the first power-off mode, in the power-off state, white level signals or black level signals are written in all the pixels while the pixels in said pixel section are first selected in a sequential manner in units of rows; and

in the second power-off mode, in the power-off state, the active elements for all

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the pixels in said pixel section are switched on and all the signal lines are set to each have a potential equal to the potential of common electrodes of the pixels.